

Amendments to the Specification:

Please replace paragraph [0074] with the following rewritten paragraph:

[0074] Subsequently, the power source line control signal ~~Sv for SFC~~ for switching the driving-voltage supplying transistor Qv to on state is supplied from the power source line control circuit 15 to the gate of the driving-voltage supplying transistor Qv through the power source line control line F. Thus, the driving-voltage supplying transistor Qv becomes on state, and then the driving voltage Vdd is supplied to the source of the driving transistor Q1.

Please replace paragraph [0097] with the following rewritten paragraph:

[0097] Subsequently, the power source line control signal ~~Sv for SFC~~ for switching the driving-voltage supplying transistor Qv to on state is supplied from the power source line control circuit 15 to the gate of the driving-voltage supplying transistor Qv through the power source line control line F. By doing so, the driving-voltage supplying transistor Qv is switched to on state, and then the driving voltage Vdd is supplied to the source of the driving transistor Q1. At that time, since the driving voltage Vdd is always supplied to the second electrode Lb of the holding capacitor Co independently, regardless of on/off states of the driving-voltage supplying transistor Qv, the variation in voltage of the holding capacitor can be prevented when the quantity of charge corresponding to the data current Idat is held in the holding capacitor Co and when the driving current Iel is supplied from the driving transistor Q1 to the organic EL element 21 by switching the driving-voltage supplying transistor Qv to on state. Therefore, the driving current Iel corresponding to the voltage Vo held in the holding capacitor Co is supplied to the organic EL element.